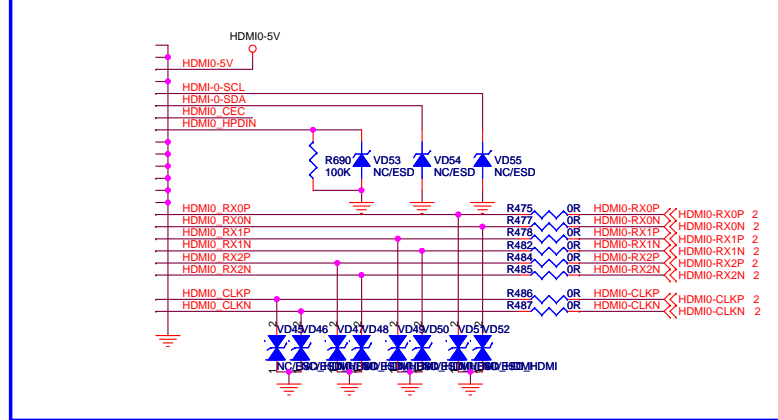
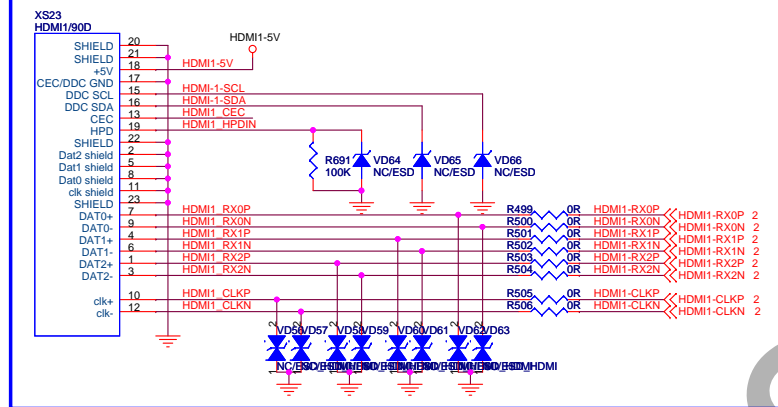
[illegible][illegible]

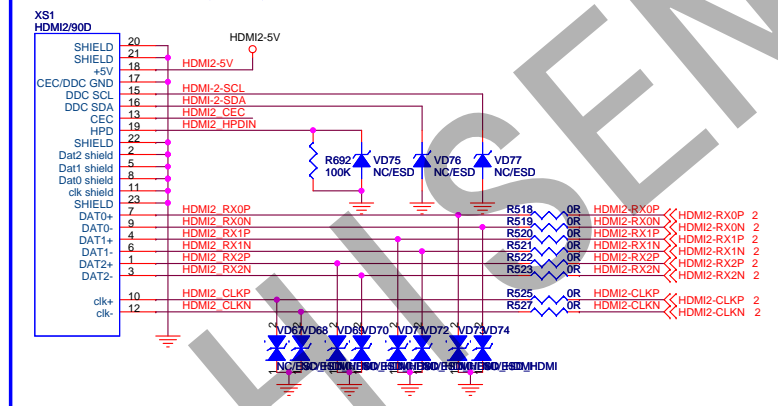
## HDMI Connector (下右)



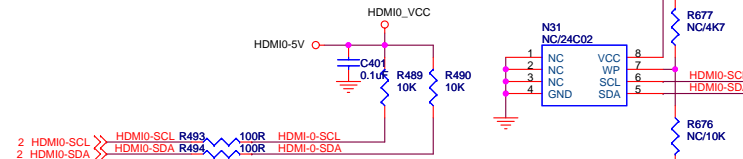
## HDMI Connector (下中)



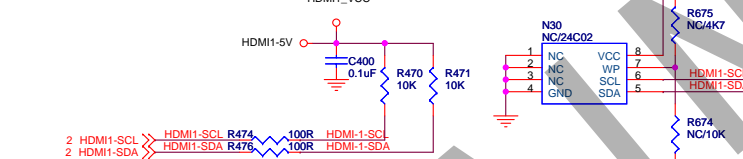
## HDMI Connector (侧面)



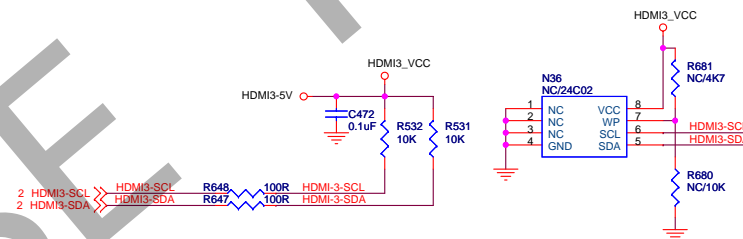
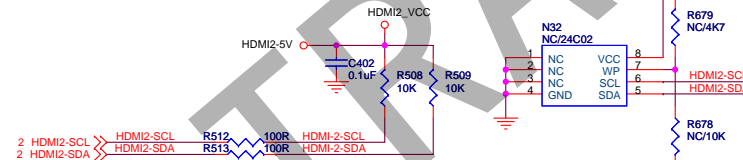
## DDC EEPROM



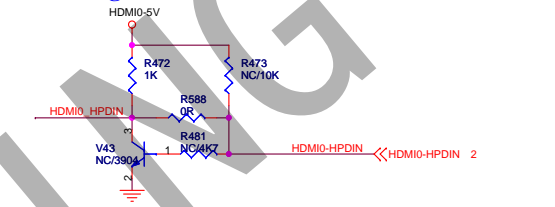
## DDC EEPROM



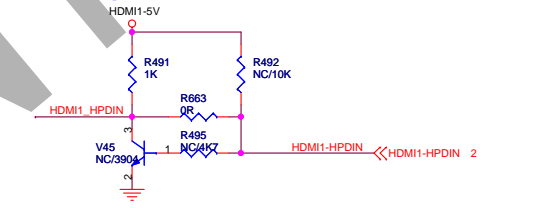
## DDC EEPROM



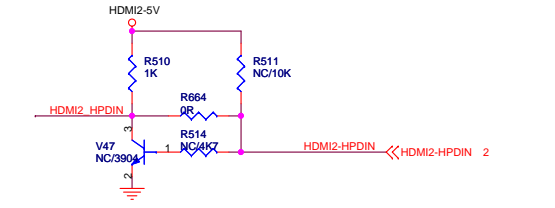
## Hot-Plug Control



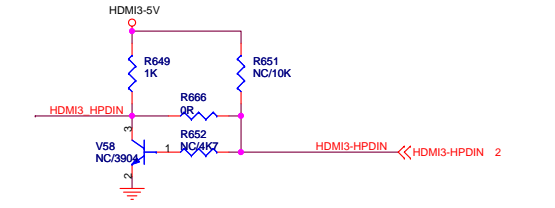
## Hot-Plug Control



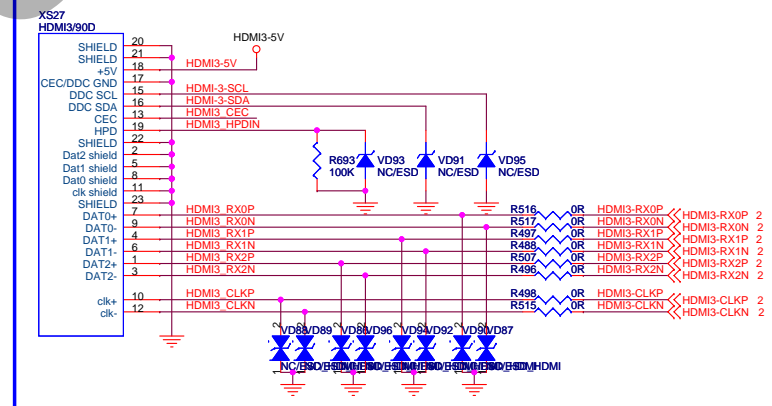
## Hot-Plug Control



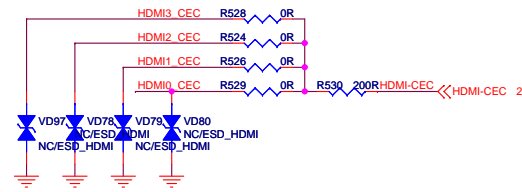
## Hot-Plug Control



## HDMI Connector (下左)

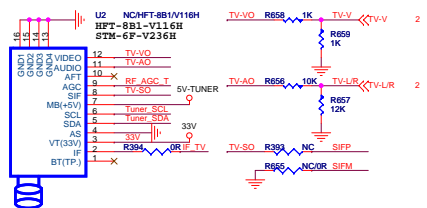


## For CEC Leakage Protect

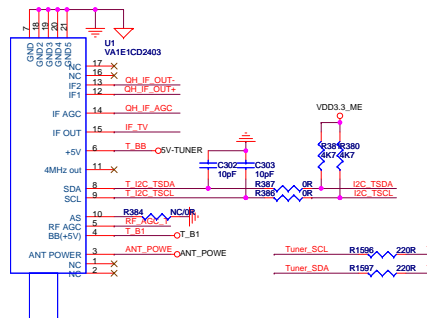




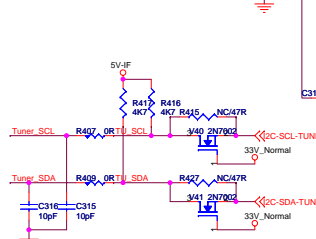
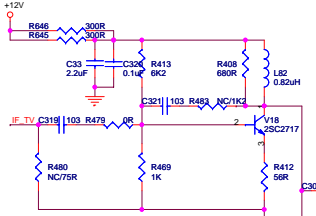




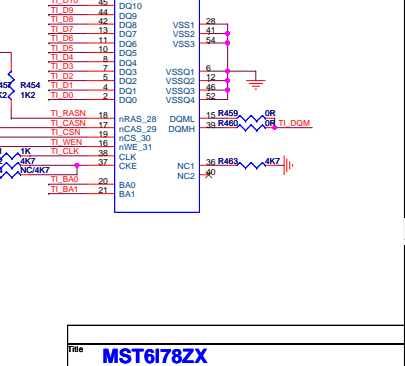
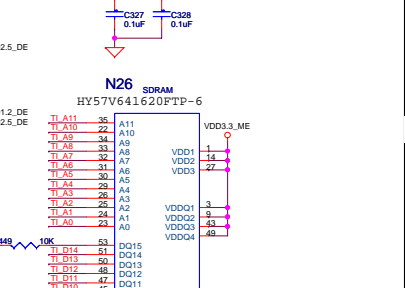
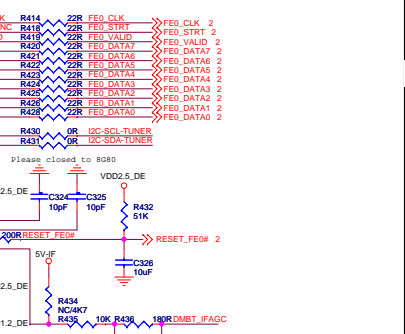
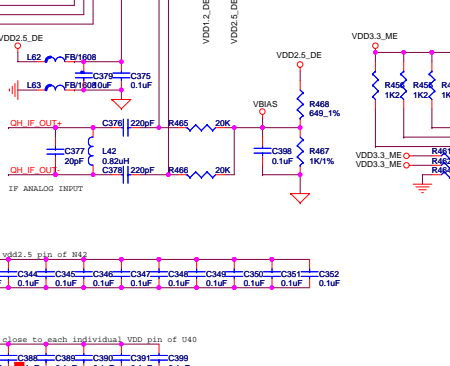
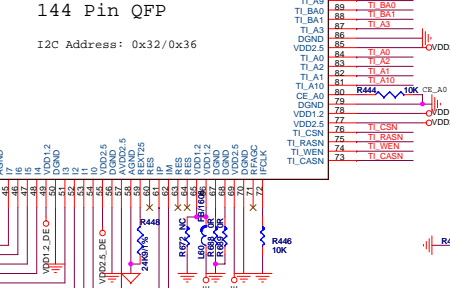
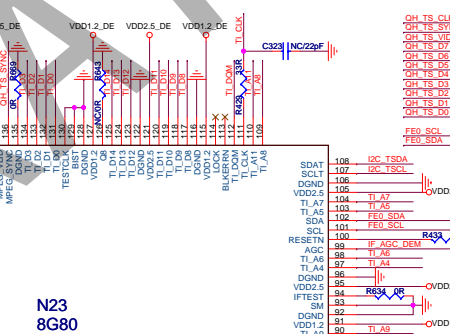
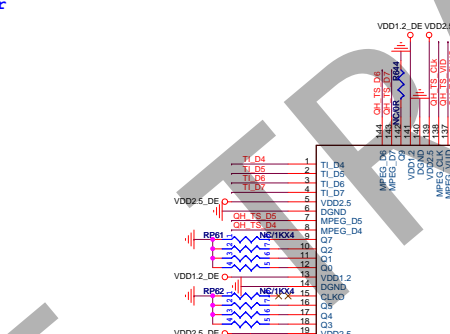
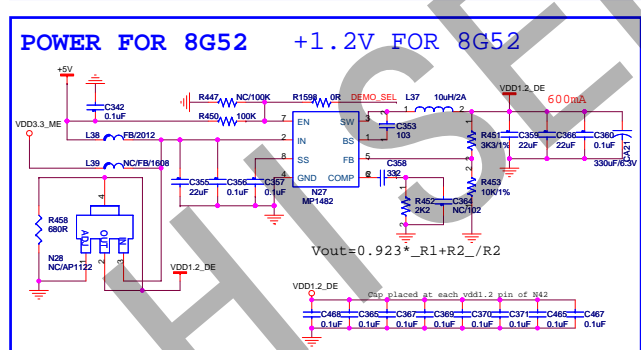
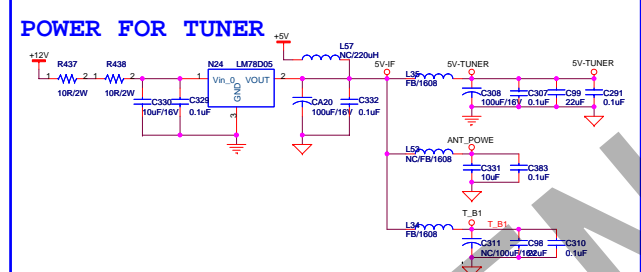
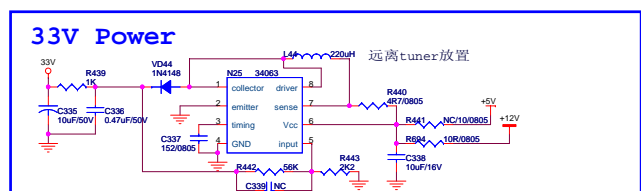
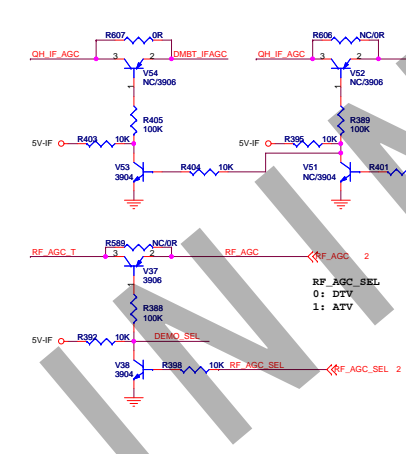
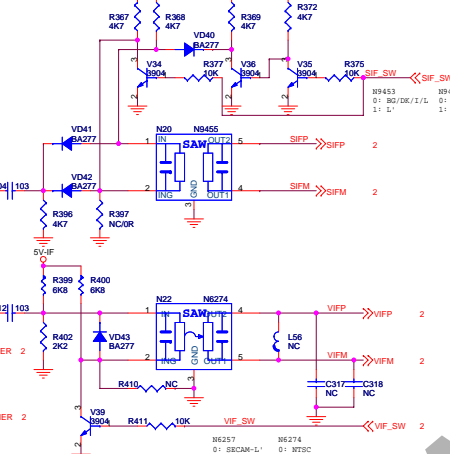
### DMBT Tuner

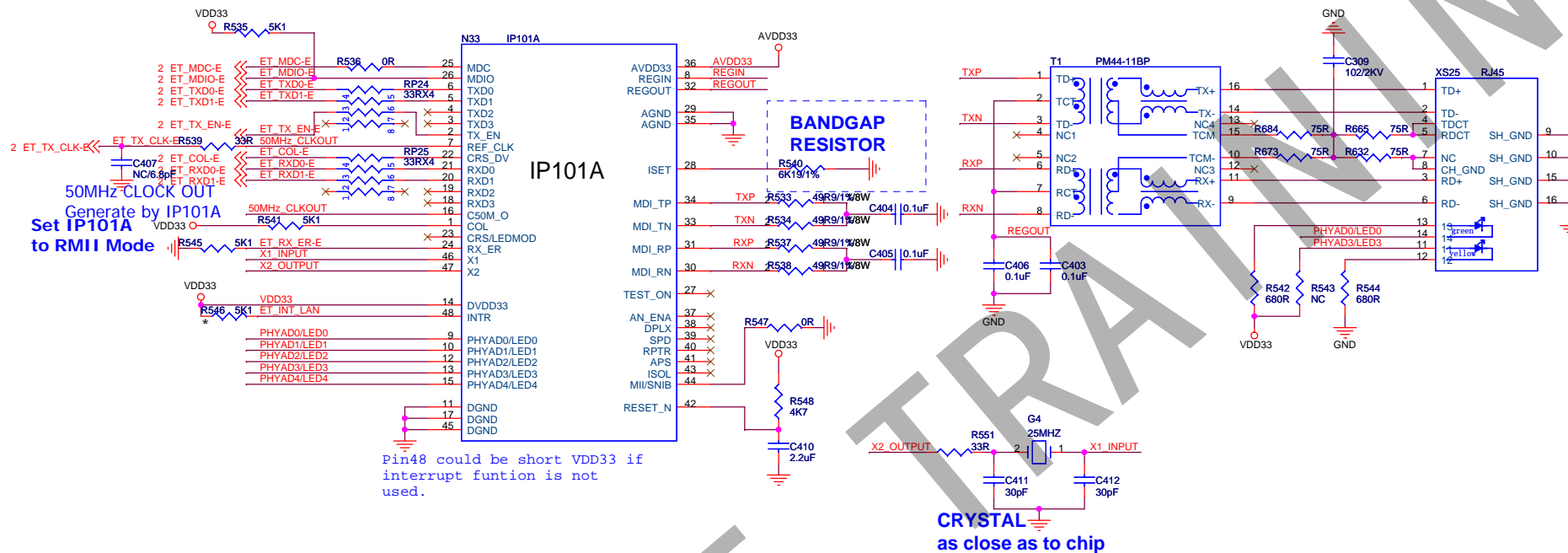


### ANALOG TUNER

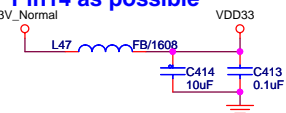


### ANALOG TUNER I2C Level Shifter

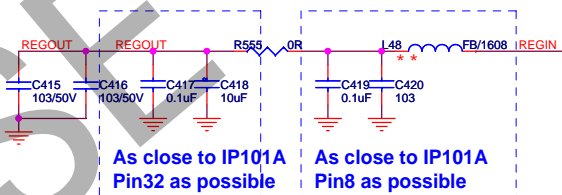
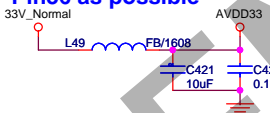




As close to IP101A  
Pin14 as possible

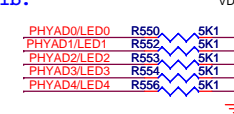


As close to IP101A  
Pin36 as possible



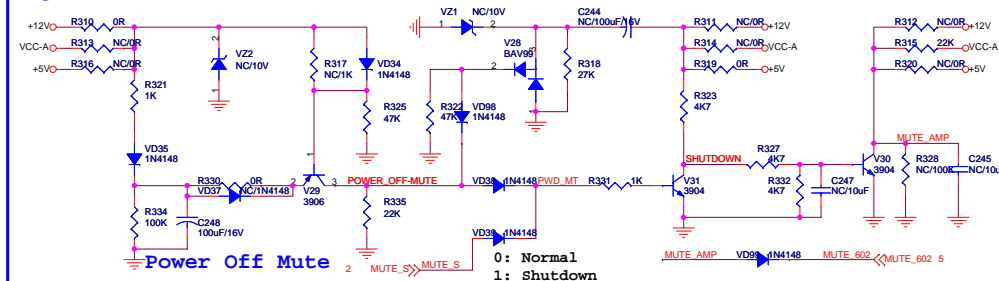
LED and PHY address  
Configuration

This schematic sets PHY address to 00001b.



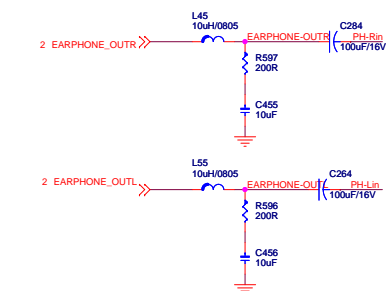
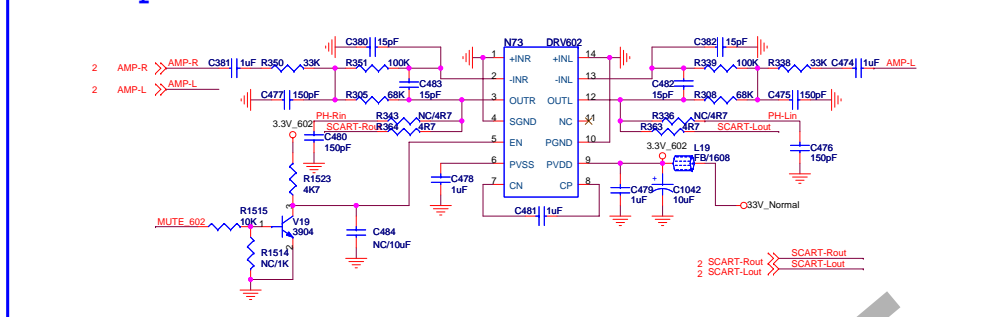
LED0	LED1	LED2	LED3	LED4
Link	Dupx	10Act	100Act	COL

## Power On Mute



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audio pre. AMP



## 耳机输出

